

Appln No. 10/621,005
Amdt date June 20, 2005

Amendments to the Specification:

On page 1, lines 6-13, please amend the paragraph as follows:

This application is a divisional of allowed Application No. 10/043,788 filed January 9, 2002, now U.S. Patent No. 6,628,149, issued September 30, 2003, which claimed the benefit of the filing date of U.S. Provisional Application Number 60/260,582 entitled "Sub-Micron, high input voltage tolerant I/O circuit" filed January 9, 2001, which is hereby incorporated by reference as though set forth in full, and is related to U.S. Patent Application No. 10/043,763, filed on January 9, 2002, now U.S. Patent No. 6,847,248, issued January 25, 2005.